

ORGANISING COMMITTEE

Patron - Prof. Rajat Gupta Director, NIT Silchar
Chairman & Branch Counselor- Prof. F.A Talukdar, ECE Dept.
Faculty Advisor of Student Chapter- Dr. T.R Lenka,ECE Dept.
Secretary- Dr. K.L Baishnab, ECE Dept. NIT Silchar
Joint Secretary-Dr. Koushik Guha, ECE Dept. NIT Silchar
Experts - VLSI Team of NIT Silchar

ADDRESS FOR CORRESPONDENCE

Dr. K.L Baishnab

Email: klb@ece.nits.ac.in / klbaishnab@gmail.com
Web: www.nits.ac.in
Contact number: 9435176506/8011884756

Sourav Nath

Email: nathsourav945@gmail.com
Contact Number: +919854922053

N.B. All the details are available in NIT Silchar website under Event Calendar.

HOW TO REACH US:

There are daily flights from kolkata, Guwahati, Imphal, and Agartala. Silchar is also well connected by train from Guwahati,Agartala,Kolkata. Buses run everyday from Guwahati Agartala and Imphal with day as well as night service.



Department of Electronics and Communication Engineering
National Institute of Technology ,Silchar
Assam,India
Organising

5 Days

WORKSHOP ON

VLSI DESIGN

Using

cādenceTM

In association with IEEE ED NIT Silchar Student Branch Chapter



From:-

1st November 2017 to 5th November 2017

cādenceTM

Venue
VLSI LAB
ECE Department
NIT Silchar,Assam



Department of Electronics and Communication Engineering
National Institute of Technology ,Silchar
Assam,India



ABOUT THE INSTITUTE

National Institute of Technology Silchar is one of the 31 National Institutes of Technology in India and was established in 1967 as a Regional Engineering college in Assam. In 2002 it was upgraded to the status of National Institute of Technology and was declared as Institute of National Importance under the national institute of technology act, 2007.

ABOUT THE DEPARTMENT

The Department of Electronics & communication offers B. Tech program in Electronics & Communication Engineering covering modern communication and software technologies. Keeping in mind the great potential and demand for new communication technologies, the Department aims at imparting its students with the latest knowledge in the field. To complement this the Department has well equipped laboratories housing various modern equipment's to enable the students to keep pace with the fast changing technology.

ABOUT THE TRAINING

The training is intended to provide a vibrant opportunity for research scholars to enrich their knowledge in the area of digital IC Design. The following topics will be covered on this training.

Analog VLSI Design

- Review of Analog VLSI design
- Introduction to Cadence design tools
- Cadence virtuoso schematic design
- Cadence virtuoso layout design
- Cadence virtuoso DRC, LVS, RCX Extraction
- Post Layout Simulation
- Monte Carlo Simulation
- GDS file extraction

Experts:-

- Dr. P.K Paul ECE Department
- Dr. K.L Baishnab ECE Department
- Dr. Koushik Guha ECE Department

TARGETED PARTICIPANTS

- B.Tech / M.Tech / PhD Students
- BSc/MSc in relevant field
- Faculty

IMPORTANT DATES

Registration is open from 9th oct. 2017

Last date for receipt of Application form will be 1st Nov. 2017

REGISTRATION

Registration fees of rupees 1000 for NIT silchar students and Rs.2000 for others.

MODE OF PAYMENT

- ❖ DD of Rs. 1000.00/ Rs.2000.00, in favour of Director NIT Silchar Payable at SBI NIT branch. Branch Code-7061.
- ❖ Online payment can be made through this channel

In favour of Director NIT Silchar
A/C No.10521277057
IFSC Code-SBIN0007061
MICR Code-788002004

(Please send the scanned copy of Registration form with payment receipt)

Post the duly filled Registration Form along with the Demand Draft to the training Co-coordinator address mentioned below.

To,

Dr. K.L Baishnab
Head of the Department
ECE Department
NIT Silchar-788010

For any query feel free to contact us in the details furnished below

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