

Registration Details

External participants have to make payment via DD of Rs. 1000/- in favour of **Director NIT Silchar, Payable at State Bank of India, NIT Silchar branch.**

For internal participants the registration fees is not required.

Brochure and registration form can also be downloaded from the institute website: <http://www.nits.ac.in>.

Submission deadline: Scan copy of Registration Form and D.D. required to be submitted via email to (sudarsan_iisc@yahoo.in) on or before **17th January, 2017**. The hard copy of original registration form forwarded by the competent authority and D.D. has to be submitted before the commencement of the workshop at the registration desk.

About NIT Silchar

National Institute of Technology (NIT) Silchar, an Institute of National Importance under the NIT Act was established in 1967 as Regional Engineering College (REC) Silchar in Assam. In year 2002, it was upgraded to the status of an NIT from REC. NIT Silchar is situated on the banks of river Barak and on a sprawling campus spread over 600 acres of land on the outskirts of Silchar. It is surrounded by beautiful lakes and hillocks. NIT Silchar is a fully residential Institution. At present it offers six undergraduate courses in Civil Engineering, Mechanical Engineering, Electrical Engineering, Electronics and Telecommunication Engineering,

Computer Science and Engineering and Electronics & Instrumentation Engineering and some AICTE approved Post-Graduate courses. The Department of Electronics & Instrumentation Engineering is established along with the inception of the Institute in 2009. Ever since its establishment the Department has been striving to provide quality technical education. The Department lays special emphasis on research and developmental activities both on the part of the faculty as well as the students.

Eligibility

This program is open to Faculty Members, Research Scholars, M.Tech./M.Sc. and BTech. Students of technical institutions, engineering colleges, polytechnics, Universities and other recognized institutions. The number of participants is limited to 50.

Accommodation and Travel

All the out station participants will be provided accommodation on payment basis in the Guest House of the Institute, subject to availability. No TA and DA will be paid to the participants.

Address for Communication

Sudarsan Sahoo,
Assistant professor, Department of E & I Engg.
NIT Silchar – 788 010
E-mail: sudarsan_iisc@yahoo.in
Contact No.: +91-8761932350

Three Days Hands-on Workshop

on

NI Multisim

(Under TEQIP-II)

18th -20th , January 2017

Coordinators:

**Prof. Sudarsan Sahoo
Prof. S.H. Laskar
Prof. Jupitara Hazarika**



ORGANIZED BY:

**DEPARTMENT OF E & I Engg.
NATIONAL INSTITUTE OF TECHNOLOGY SILCHAR
SILCHAR-788010, ASSAM-INDIA**

Introduction

The National Instruments circuit design suite (Multisim and Ultiboard) equips the professional PCB designer with world class tools for schematic capture, interactive simulation, board layout, and integrated test. This course teaches the fundamentals of the Multisim integrated capture and simulation design environment.

Multisim simulation and circuit design software gives engineers the advanced analysis and design capabilities to optimize performance, reduce design errors and shorten time to prototype. With powerful learning features and laboratory hardware integration, multisim teaches students fundamental concepts in analog, digital and power electronics throughout engineering and science curriculum.

Course Objectives

NI Multisim Basics: Schematic Capture and Simulation.

Course Contents

- Understand the Multisim user interface.
- Use Multisim to capture circuit schematics.
- Use interactive simulation to check your design.
- Use virtual instruments and analyses.
- Apply modular design with sub circuits, hierarchical blocks or multipage designs.
- Blocks or multipage designs.
- Create custom title blocks.
- Properly document your circuit designs.
- Work with design variants.
- Create custom components.
- Simulate MCU projects along with SPICE.
- Transfer your design to PCB layout software.

Course Material

Each registered participant will be provided with a set of lecture notes (soft copy).

Organising Committee

Patron:

Prof. Rajat Gupta,
Director, NIT Silchar

Chairman:

Dr. Rajdeep Dasgupta,
Head, Department of E & I, NIT Silchar

Coordinators: Prof. Sudarsan Sahoo
Prof. S.H. Laskar
Prof. Jupitara Hazarika

Organizing Members:

- Prof. Lalu Seban
- Prof. Arun Sunaniya
- Prof. Munmun Khanra
- Prof. Swagatadeb Sahoo
- Prof. Manas Ku. Bera
- Prof. Ranjay Hazra
- Prof. Sandip Kumar
- Mr. Kalimudin Mondal
- Mr. Tushar Bachhar
- Mr. Chandan Dawo

REGISTRATION FORM

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NI Multisim

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(18-20, January, 2017)

Organized By: **Department of E & I Engg.**

NIT SILCHAR, ASSAM-788010

1. Name (block letter):

2. Designation:

3. Organization:

4. Address for communication: _____

_____ Pin code: _____

5. Ph. No.:

6. E-mail:

7. Educational Qualification:

8. Accommodation Required: Yes / No

Place:

Date:

Signature of the applicant