



**Department of Electronics & Communication Engineering**  
**National Institute of Technology Silchar**  
**Silchar - INDIA**

**Advertisement No: NITS/Dean (RC)/ECE/640/02/2023**

**Date: 27-12-2023**

Applications are invited from Indian citizens for a temporary post of **Project Associate-I (PA-I)** in the Department of Electronics & Communication Engineering, NIT Silchar, Assam, for the following project.

Title of the project	<i>Design and Development of Low Power Low Latency Non-Invasive Seizer Detector System.</i>
Sponsored Agency	Ministry of electronics & Information Technology (MeitY), Government of India
Projects action number	EE-9/2/2021-R&D-EDated 22 May 2023
No. of posts	01(one)
Duration	1 years and extendable till the completion of the project (10/02/2027)
Principal Investigator (P.I & Co-PI)	Prof. K.L. Baishnab & Dr. Koushik Guha

General Information and Terms & Conditions

1	Essential Qualification and experiences	M.E/M. Tech in Microelectronics & VLSI Design with minimum 8.0 CGPA with good academic record from a recognized University/Institute.
2	Desirable Experience for both the posts	Knowledge of working in Linux environment, Knowledge of FPGA, Knowledge of RTL Coding in Verilog, Knowledge of FPGA Implementation, Knowledge of biological background related to the project work.
3	Salary	<b>Rs.31000/- per month.</b> HRA along with other allowances/increment will be provided as per project/institute Norms. Candidates may also get an opportunity to pursue higher studies as per Institute Norms.
4	Last date of receiving/ Submission of filled up application form	<b>15<sup>th</sup> January, 2024</b>
5	Reporting	Applicants are requested to submit the signed scanned copy of their filled-in application form along with CV and the all supporting documents to the P.I. through email at <a href="mailto:klb@ece.nits.ac.in">klb@ece.nits.ac.in</a> and <a href="mailto:koushik@ece.nits.ac.in">koushik@ece.nits.ac.in</a> with subject line "Application for the post of Project Associate-I under MeitY project EE-9/2/2021-R&D-E". Call letter for shortlisted candidates will be sent through email.
6	Date of interview, time, And venue	<b>Interview date and mode (online/offline) will be notified in due course of time through email.</b>
7	No objection certificate	Employed candidates are to submit no objection certificate from his/her Employer at the time of interview.
8	Institute right	The Institute has the discretion to restrict the number of candidates to be called for the interview on the basis of screening. No candidate may be recruited if suitable candidates are not found.
9	Signing of the agreement	Selected candidates shall have to sign an agreement at the time of joining.

Sd/-  
Associate  
Dean(R&C) NIT  
Silchar.



National Institute of Technology, Silchar

FORMAT of APPLICATION

For Project Associate-I

Pasterecent  
passport  
sized colored  
photograph

1. Advertisement no..... dated:
2. Title of the Project- .....
- .....

3. Department:

4. Name of the applicant(in block letters):

5. Father's/Husband's Name:

6. Date of Birth(DD-MM-YYYY):

7. Postal Address:

E-mail:

Phone no.:

8. Permanent Address:

9. Nationality:

10. Marital Status:

11. Whether belongs to SC/ST/PH/OBC:(If yes, attach certificate):

12. Educational Qualification: (Starting from Class X)

Exam Passed	Board/ University	Division	%age of Marks/CG PA	Year of Passing	Subjects

13. Details of employments:

Sl. No.	Name of the Organization	Name of the post	Period	Scale of pay and present pay	Nature of duty

14. Any other relevant information that you may like to furnish:

**Declaration**

I declare that the above information is true and correct to the best of my knowledge and belief.

Date:

Place

Signature of the applicant